

ISTE Approved Short Term Training  
Program

on

**“Signal Processing &  
Applications”**

JULY 6-12, 2009

- Organized by -

**VIT**

Vidyalankar  
Institute of  
Technology

**Vidyalankar Institute of Technology**

Wadala, Mumbai - 400 037

**Chief Patron**

Mrs. Sanjeevani Deshpande  
(Chairperson)

**Vidyalankar Dnyanapeeth Trust**




## **THE INSTITUTE**

Vidyalankar Institute of Technology was established in the year 1999 with a mission to provide an educational environment where students can reach their full potential in their chosen discipline and as individuals.

The institute also provides a scholarly environment where imparting knowledge through the dedicated team of experienced faculty, inculcates good ethics in the student community. Our students are placed in world famous universities and organizations.

Vidyalakar Institute of Technology is an "A" Grade college, which is affiliated to the University of Mumbai and approved by AICTE. We conduct courses in Electronics, Electronics & Telecommunication, Computer, Biomedical Engineering and Information Technology with an average intake of 420 students per year in the undergraduate level. The college also conducts a course in ME Computer Science and Masters in Management Studies at the postgraduate level.





## **ABOUT THE COURSE**

Signal Processing is vital for Electronics , Telecommunication & Computer related fields.

The present Course elaborate its knowledge in Signal Processing and its Applications.

The course covers recent advances in Signal Processing & its Applications.

Some of the topics covered are

- ❖ Image Processing
- ❖ Speech Processing
- ❖ Radar Signal Processing
- ❖ Wireless & Mobile application
- ❖ D.S.P Algorithms
- ❖ Biomedical Applications
- ❖ DSP/IP tools

## **COURSE FACULTY :**

Eminent academician Dr. Kekre,

Dr. Bhosale, Dr. Mukherjee,

Dr. Tucklay (Sameer, IIT) and other experts from IITB, premier industries, Prestigious engineering colleges and VIT will be engaging the sessions.





## **ELIGIBILITY:**

The course is open to professionals from industry as well as members of the faculty from AICTE approved engineering colleges across the country, possessing a basic degree in any of the following branches: Electronics & Telecommunication, Instrumentation, Electrical, Electronics, Biomedical, Computer and Information Technology. The seats are limited to 40.


## **REGISTRATION FEE:**

Participants from AICTE approved institutions:  
Rs. 1500/-

Participants from Industry:  
Rs. 3000/-

## **MODE OF PAYMENT:**

All payments should be made through crossed Demand Draft, drawn in favor of Vidyalankar Institute of Technology, payable at Mumbai.





## **CONTACT DETAILS**

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### **Dr. Prashant Sonare**

Principal, VIT  
(Co-ordinator)

## **Vidyalankar Institute of Technology**

Wadala, Mumbai - 400 037

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**Registration Form**  
**ISTE Approved Workshop on**

**“Signal Processing Applications”**  
**(July 6-12, 2009)**

At

**Vidyalankar Institute of Technology**  
Mumbai

Name \_\_\_\_\_

Designation \_\_\_\_\_

Qualification \_\_\_\_\_

Experience \_\_\_\_\_

ISTE membership No. \_\_\_\_\_

Institution \_\_\_\_\_

Mailing address \_\_\_\_\_

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Telephone (O) \_\_\_\_\_

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E-mail \_\_\_\_\_

DD No. \_\_\_\_\_

Bank \_\_\_\_\_

Signature of the applicant \_\_\_\_\_

Date : \_\_\_\_\_ Place : \_\_\_\_\_

Signature of the forwarding authority with stamp

Date: \_\_\_\_\_ Place: \_\_\_\_\_

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Last Date for Registration: June 25<sup>th</sup>, 2009